



Attorney Docket No. 0756-1950

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 09/285,899

Filed: April 8, 1999

For: ELECTRO-OPTICAL DEVICE

HAVING LEVELING FILM

) Group Art Unit: 2871

) Examiner: M. Ton

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*Ashli M. Stamps*

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In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

A full English translation of JP 59-194783 is submitted herewith. An English abstract of this reference was previously submitted with the Information Disclosure Statement of July 26, 2005.

Respectfully submitted,

*Eric J. Robinson*

Eric J. Robinson

Reg. No. 38,285

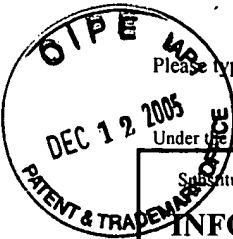
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				Application Number	09/285,899
				Filing Date	April 8, 1999
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	2871
				Examiner Name	T. Ton
Sheet	1	of	1	Attorney Docket Number	0756-1950

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		Office <sup>2</sup>	Number <sup>4</sup>	Kind Code <sup>3</sup> <i>(if known)</i>				
		JP	2-210330			08/21/1990		Full
		JP	1-156725			06/20/1989		Full

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
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Atty. Docket No.: 0756-1950

Serial No. 09/285,899

# INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Applicant: Shunpei YAMAZAKI et al.

**Filing Date:** April 8, 1999

Group: 2871

## U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
6		56-81972	07/04/81	Japan			Abst.	
6		58-32466	02/25/83	Japan			Abst.	
6		58-124273	07/23/83	Japan			Abst.	
6		58-158967	09/21/83	Japan			Abst.	
6		58-115864	07/09/83	Japan			Abst.	
6		59-214262	12/04/84	Japan			Abst.	
6		62-124769	06/06/87	Japan			Abst.	
6		1-161316	06/26/89	Japan			Abst.	

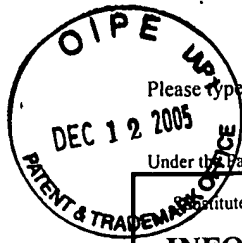
**OTHER DOCUMENTS** (Including Author, Title, Relevant Pages, Date, Place of Publication)

①	Wolf et al., Silicon Processing for the VLSI ERA, Lattice Press, Volume 1, 1989, pages 151-154
②	Wolf et al., Silicon Processing for the VLSI ERA, Lattice Press, Volume 2, pages 66-72
Examiner	Date Considered 11/6/80

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		JP	59-194783		12/25/1984			Full

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